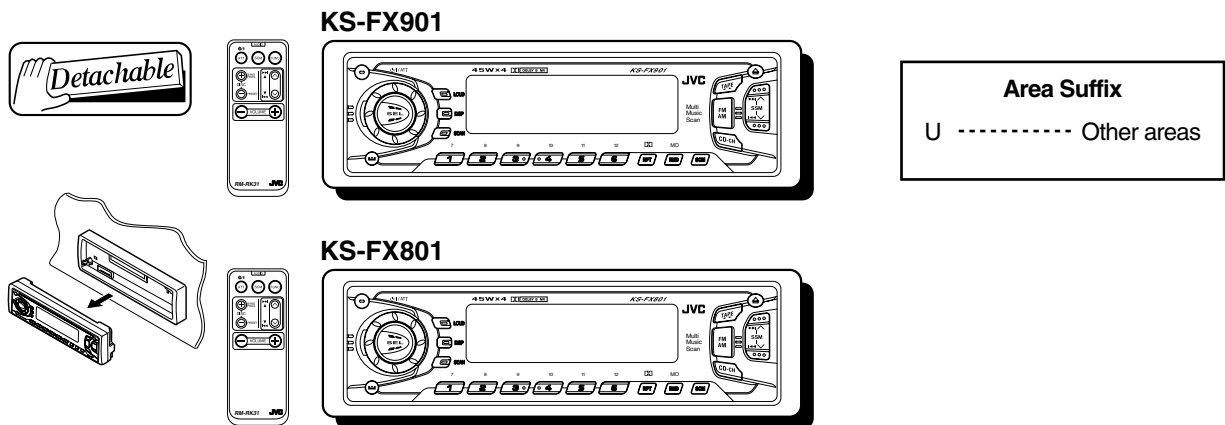


JVC

SERVICE MANUAL

CASSETTE RECEIVER

KS-FX901/KS-FX801




Difference point	Front panel	Trim plate	LCD color
KS-FX901	Light Gold	Light Gold	Multi color
KS-FX801	Metallic Gray	Gray	White

Contents

Safety precaution	1- 2
Disassembly method	1- 3
Adjustment method	1-11
Description of major ICs	1-15~25

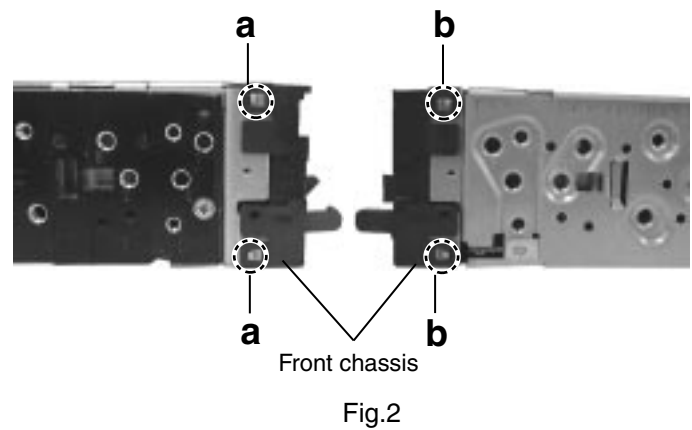
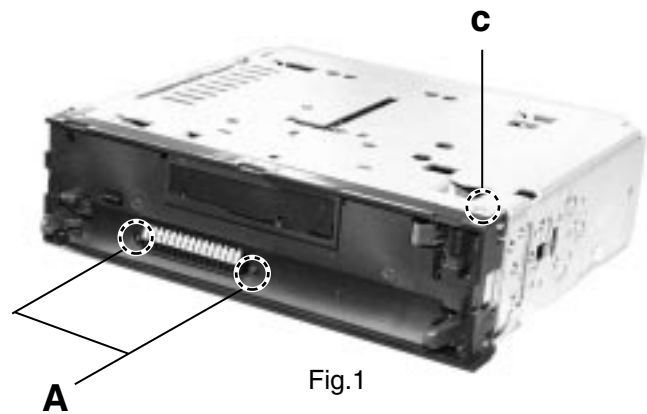
Safety precaution

 **CAUTION** Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of performing repair of this system.

Disassembly method

■ Removing the front chassis (See Fig.1~2)

1. Remove the two screws **A** attaching the front chassis.
2. Insert a screwdriver to the two joints **a** on the side of the front chassis, two joints **b** on the right side and one joint **c** from upside, then detach the front chassis toward the front side.



■ Removing the heat sink (See Fig.3)

1. Remove the three screws **B** attaching the heat sink on the left side of the body, and remove the heat sink.

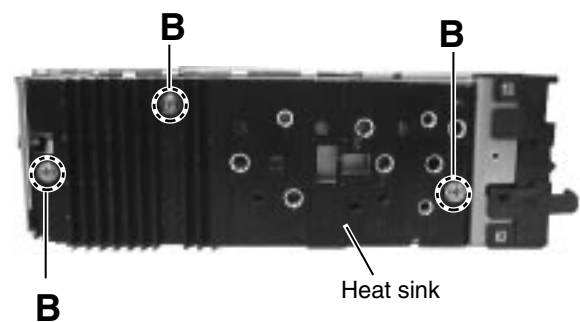


Fig. 3

■ **Removing the bottom cover (See Fig.4)**

1. Turn the body upside down.
2. Insert a screwdriver to the two joints **d** and two joints **e** on both sides of the body and the joint **f** on the back of the body, then detach the bottom cover from the body.

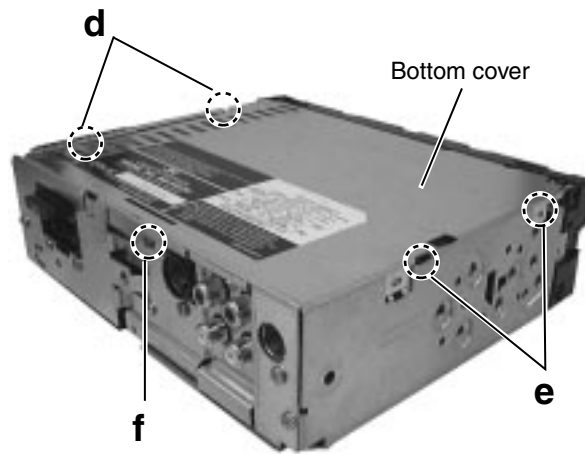


Fig. 4

■ **Removing the rear panel (See Fig.5)**

1. Remove the front chassis.
2. Remove the heat sink.
3. Remove the bottom cover.
4. Remove the seven screws **B** attaching the rear panel and one screw **C** attaching the pine jack on the back of the body.

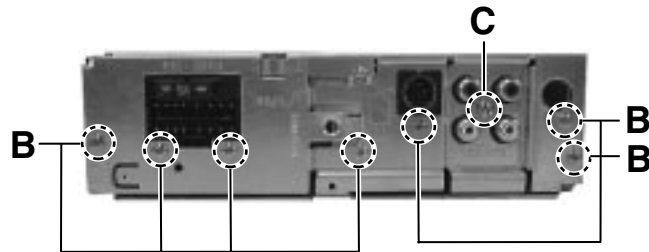


Fig. 5

■ **Removing the main amplifier board assembly (See Fig.6)**

1. Remove the front chassis.
2. Remove the bottom cover.
3. Remove the rear panel.
4. Remove the two screws **D** attaching the main amplifier board assembly on the top cover.
5. Disconnect connector CP701 on the main amplifier board assembly from the cassette mechanism assembly.

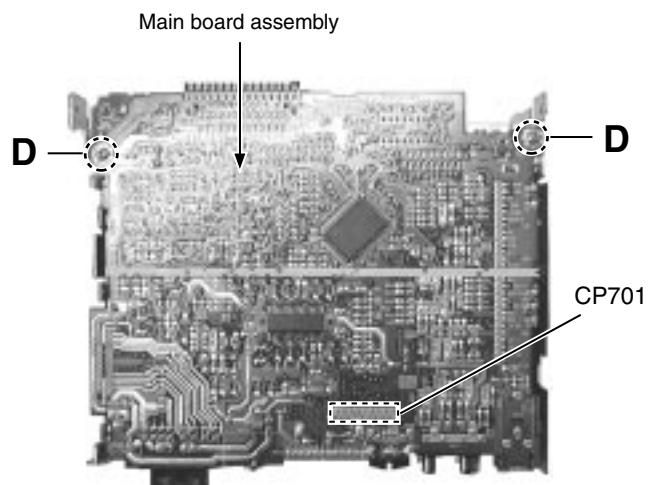


Fig. 6

■ Removing the Cassette mechanism assembly (See Fig.7)

1. Remove the front chassis.
2. Remove the bottom cover.
3. Remove the main amplifier board assembly.
4. Remove the fore screws **F** attaching the cassette mechanism assembly from the top cover.

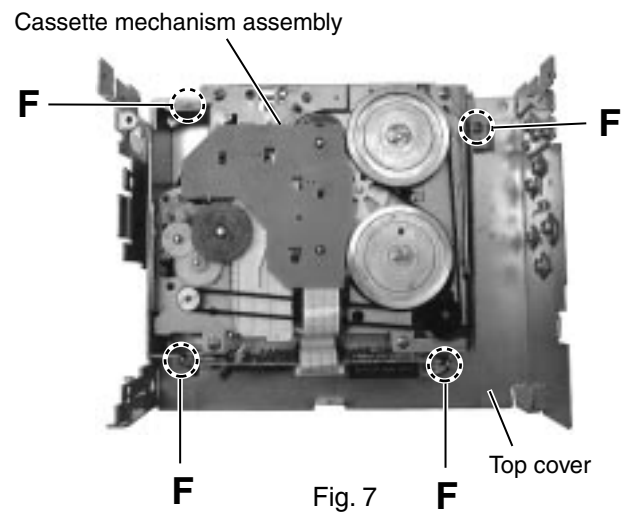


Fig. 7

■ Removing the control switch board (See Fig.8 and 9)

1. Remove the front panel unit from the main body.
2. Remove the four screws **G** attaching the rear cover on the back of the front panel unit.
3. Remove the control switch board from the front panel unit.



Fig. 8

LCD & Key control board

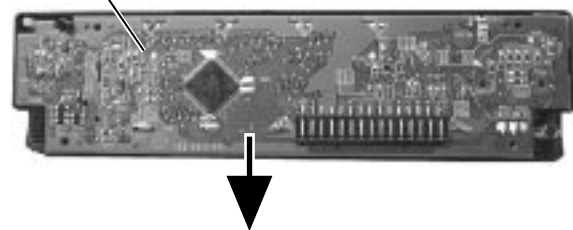


Fig. 9

<Removal of the cassette mechanism>

**■ Removing the head amplifier board.
(See Fig.1 and 2)**

1. For the 6pin wire extending from connector CN402 on the head amplifier board, disconnect it from the head relay board.
2. Disconnect the card wire from connector CN403 on the head amplifier board.
3. Remove the screw **A** attaching the head amplifier board.
4. Move the tab **a** as shown in Fig.2 and remove the head amplifier board while moving it in the direction of the arrow.

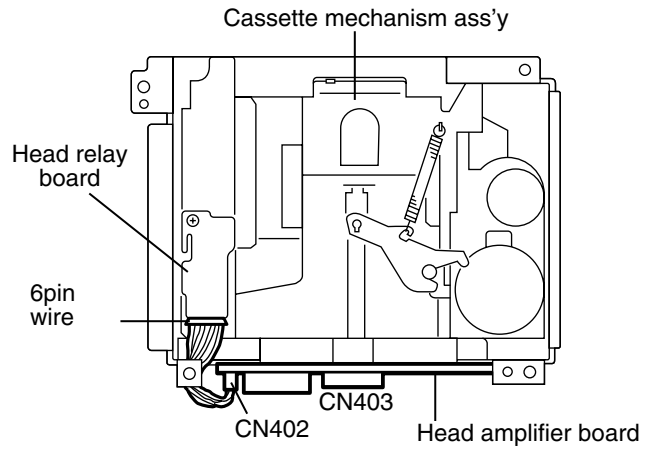


Fig.1

■ Removing the cassette mechanism assembly (See Fig.1 to 3)

1. Disconnect the 6pin wire from connector CN402 and the card wire from CN403 on the head amplifier board (Refer to Fig.1 and 2).
2. Remove the four screws **B** on the bottom of the cassette mechanism.

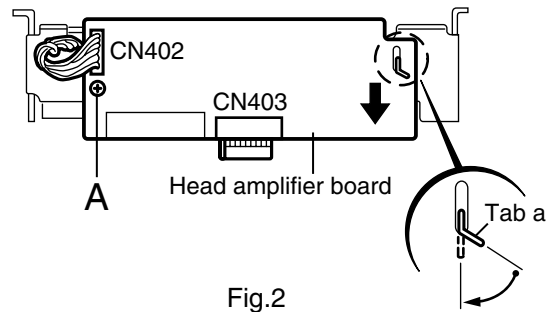


Fig.2

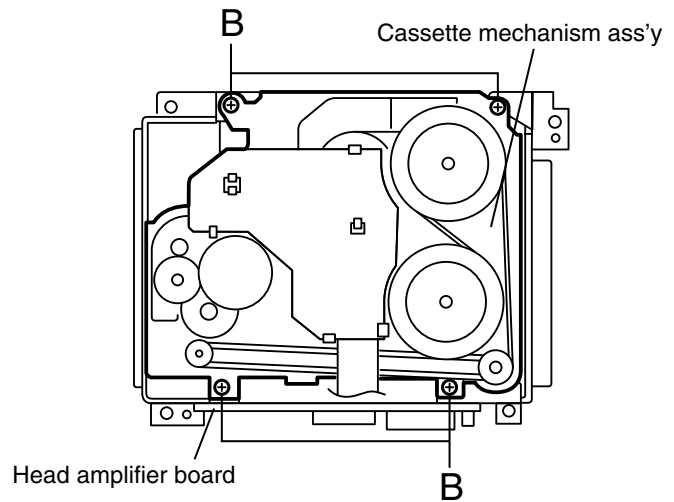


Fig.3

■ Removing the head relay board

(See Fig.4)

1. Unsolder the soldering **b** on the head relay board.
2. Remove the screw **C** attaching the head relay board.
3. Remove the head relay board in the direction of the arrow while releasing the two joints **c**.

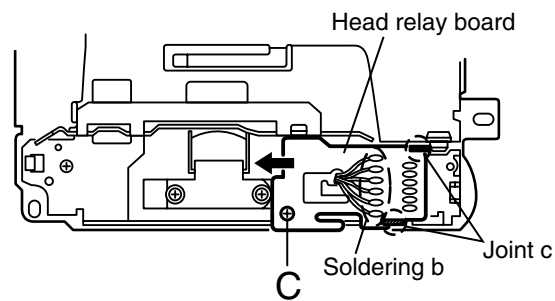


Fig.4

■ Removing the load arm (See Fig.5)

1. Remove the **E** washer attaching the load arm using a pincette or something like that and remove the spring **d**.
2. Move the part of the load arm marked ※ upwards to release it from the axis of rotation. Then rotate the load arm in the direction of the arrow to remove it from the cach.

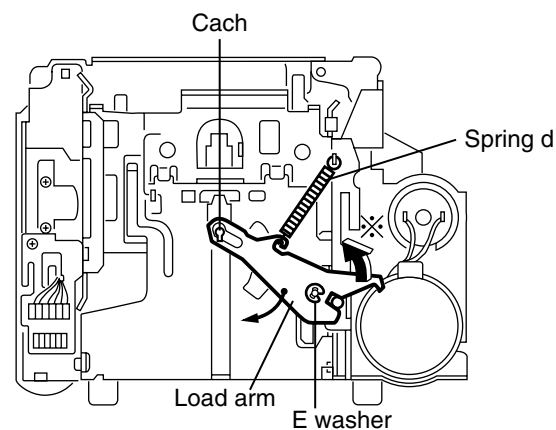


Fig.5

■ Removing the sub chassis (See Fig.6)

- Prior to performing the following procedure, remove the head relay board.

1. Remove the screw **D** attaching the sub chassis.
2. Push the tab **e** in the direction of the arrow to detach the one side of the sub chassis. Then release the sub chassis from the tab **f**.

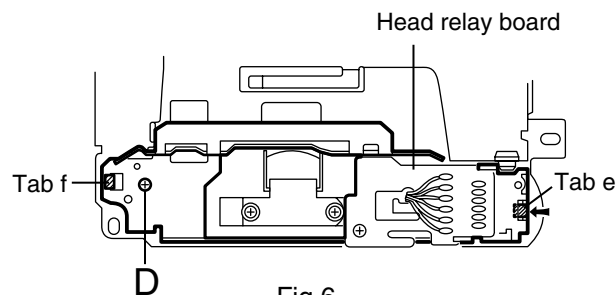


Fig.6

■ Removing the cassette holder and the holder arm in the eject mode

(See Fig.7 and 8)

- Prior to performing the following procedure, remove the head relay board, the load arm and the sub chassis.

1. Remove the screw **E** attaching the reinforce bracket.
2. Remove the reinforce bracket.
3. Push the tab **g** fixing the cassette holder in the direction of the arrow and open the cassette holder and the holder arm upward until they stop at an angle of 45 degrees. Move the two joints **h** to the side and remove the cassette holder and the holder arm from the shaft.

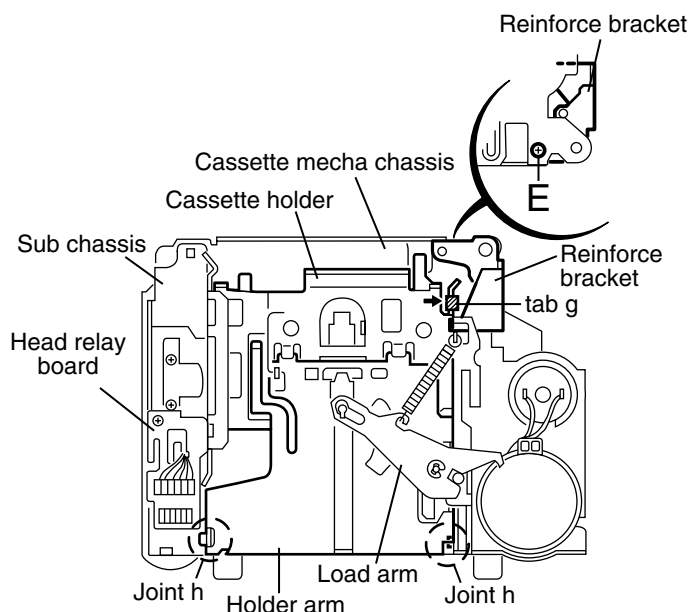


Fig.7

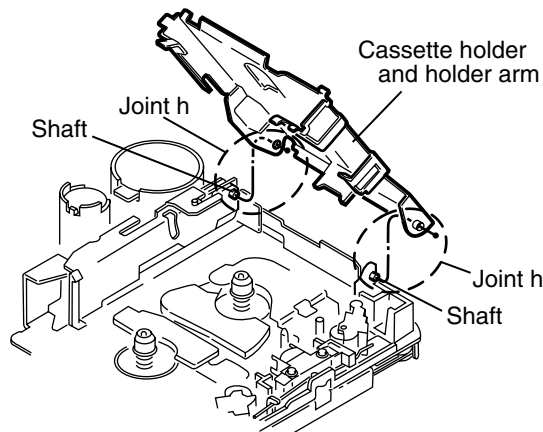


Fig.8

■ Removing the play head (See Fig.9)

• Prior to performing the following procedure, remove the head relay board and the sub chassis.

1. Remove the two screws **F** attaching the play head (The spring under the play head comes off at the same time).

■ Removing the pinch roller ass'y (See Fig.9)

• Prior to performing the following procedure, remove the head relay board and the sub chassis.

1. Push each tab **i** in the direction of the arrow and pull out the pinch rollers on both sides.

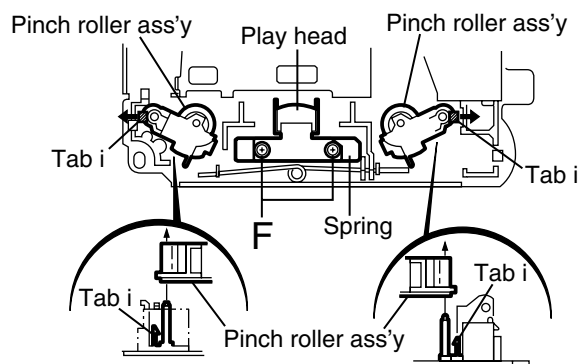


Fig.9

■ Removing the reel disc board (See Fig.10)

1. Unsolder the soldering **j** on the reel disc board.
2. Push the seven tabs **k** on the bottom of the cassette mechanism assembly in the direction of the arrow.

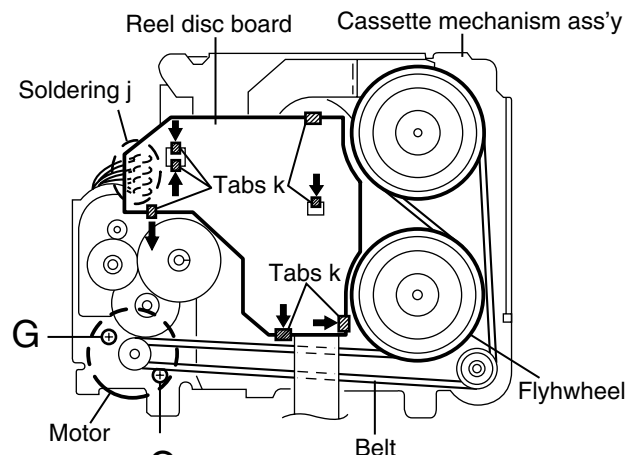


Fig.10

■ Removing the motor and the sub motor (See Fig.10 and 11)

1. Unsolder the two soldering **l** of the motor and the sub motor.
2. Release the sub motor from the three tabs **m**. Push the sub motor upward and pull out it.
3. Remove the belt on the bottom of the cassette mechanism assembly and remove the two screws **G** attaching the motor.

ATTENTION: The motors can be detached before removing the load arm.

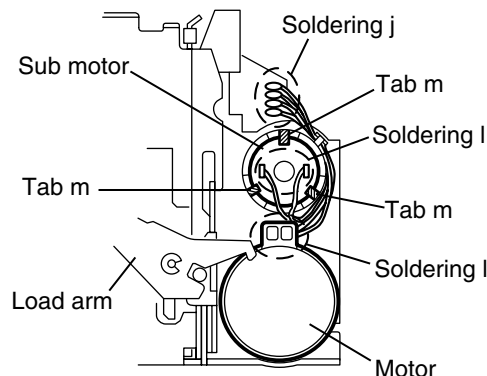


Fig.11

■ Removing the flywheel

(See Fig.10 and 12)

1. Prior to performing the following procedure, remove the head relay board, the load arm, the sub chassis, the cassette holder, the holder arm and the reel disc board.
2. Remove the belt on the bottom of the cassette mechanism ass'y.
3. Remove the slit washer attaching the flywheel on the upper side of the cassette mechanism ass'y and pull out the flywheel downward. Then remove another flywheel in the same way.

ATTENTION: When reassembling, make sure to use a new slit washer.

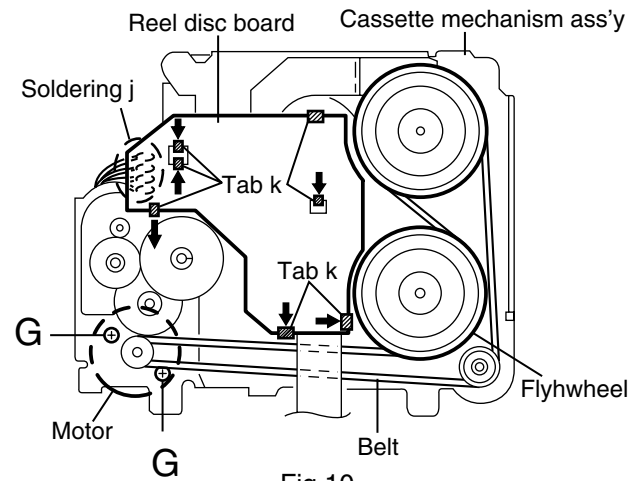


Fig.10

■ Removing the reel disc ass'y (I)

(See Fig.12 to 14)

- Prior to performing the following procedure, remove the head relay board, the load arm, the sub chassis, the cassette holder and the holder arm.
1. Disengage the part n inside of the reel driver which engages with the shaft, using a pincette or something like that. Then remove the reel driver from the shaft.
 2. Remove the reel driver spring and the reel table.

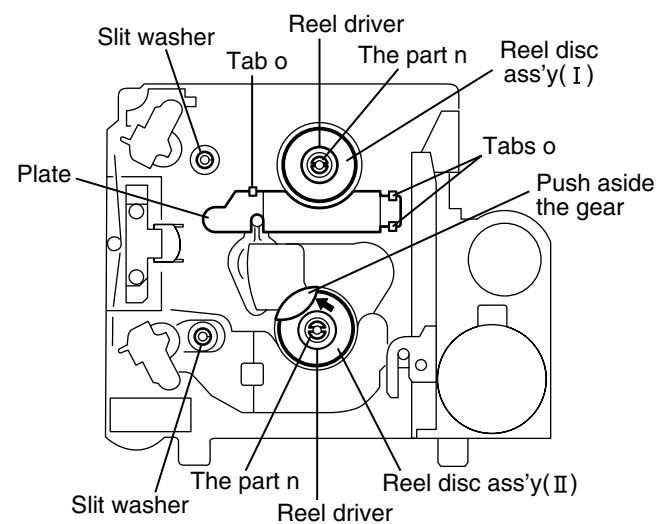


Fig.12

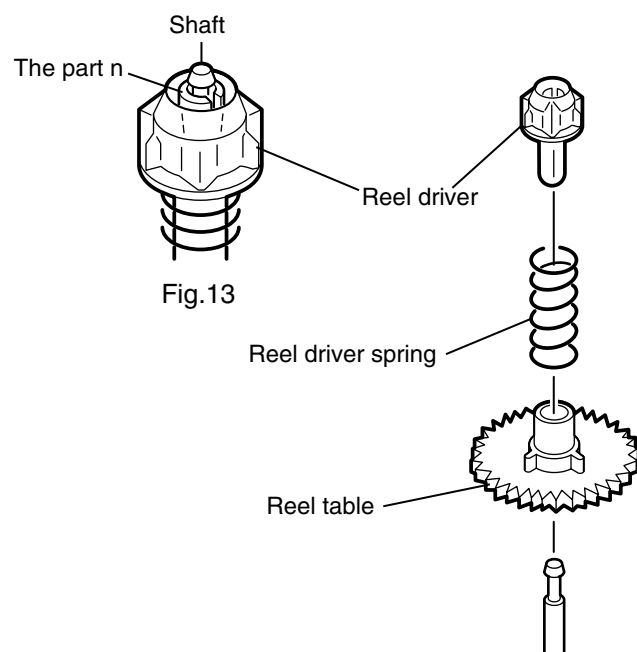


Fig.13

Fig.14

■ **Removing the reel disc ass'y(II)**
(See Fig.12 to 15)

ATTENTION: Prior to performing the following procedure, remove the reel disc (I).

1. Release the plate from the three tabs **o**.
2. Push aside the gear over the reel table using a pincette or something like that.
3. Remove the reel disc ass'y (II) as with the reel disc ass'y (I).

ATTENTION: Do not break the front panel tab fitted to the metal cover.

Push aside the gear and reattach the reel disc Ass'y(I).

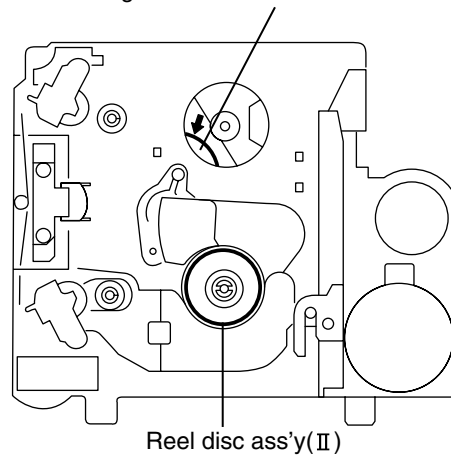


Fig.15

Adjustment method

■ Test instruments required for adjustment

1. Digital oscilloscope(100MHz)
2. Frequency Counter meter
3. Electric voltmeter
4. Wow & flutter meter
5. Test Tapes
 - MC-109C For TAPA CURL confirmation
(without Padd type)
 - VT724 For DOLBY level measurement
 - VT739 For playback frequency measurement
 - VT712 For wow flutter & tape speed measurement
 - VT703 For head azimuth measurement
6. Torque gauge Cassette type for CTG-N
(mechanism adjustment)

■ Measuring conditions(Amplifier section)

- Power supply voltage DC14.4V (10.5 - 16V)
- Load impedance $4\ \Omega$ (2Speakers connection)
- Line out $20k\ \Omega$

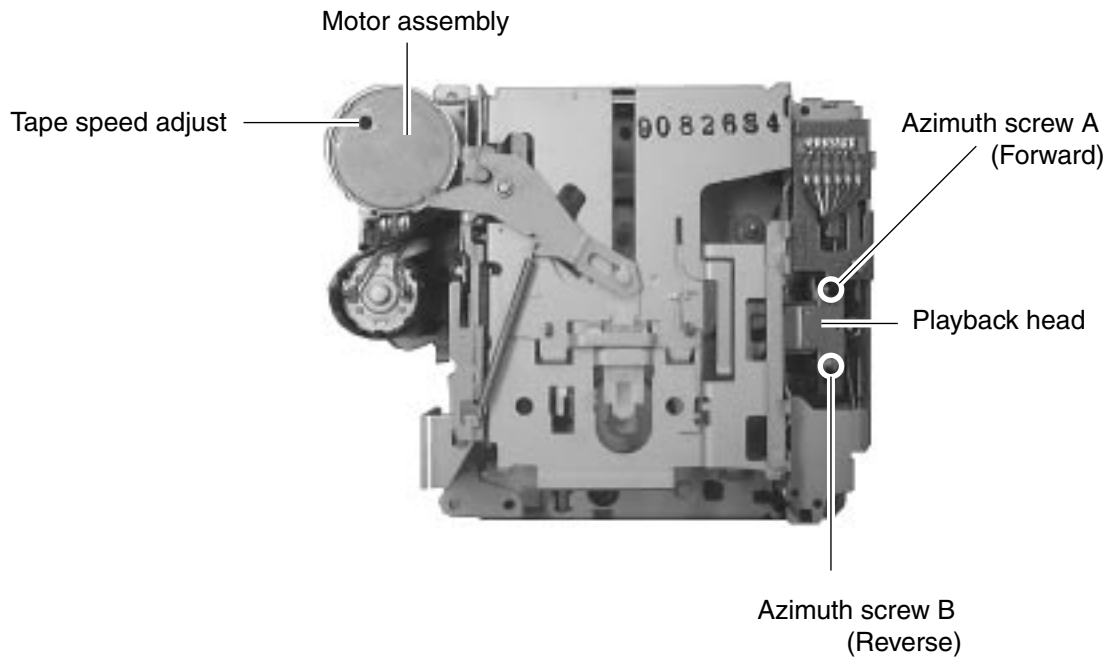
■ Standard volume position

Balance and Bass, Treble volume .Fader
:Center(Indication"0")
Loudness, Dolby NR, Sound, Cruise:Off
Volume position is about 2V at speaker output with
following conditions. Playback the test tape VT721.

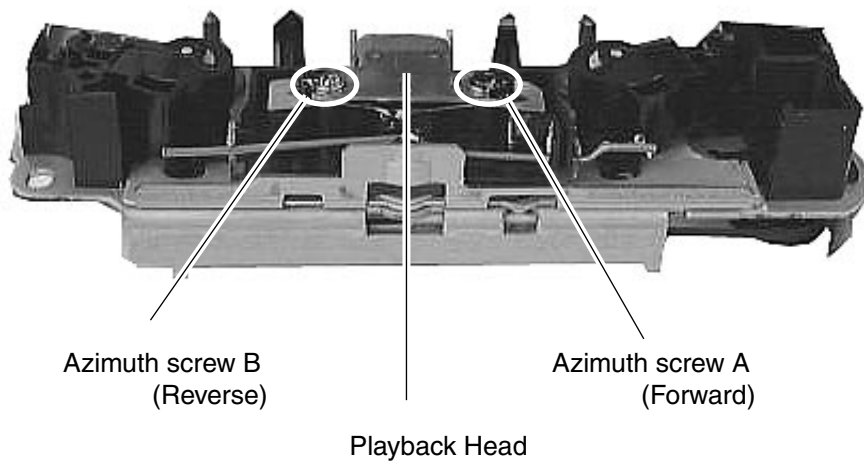
- AM mode 1000kHz/62dB, INT/400Hz, 30%
modulation signal on receiving.
- FM mono mode 97.5MHz/66dB, INT/400Hz, 22.5kHz
deviation pilot off mono
- FM stereo mode 1kHz, 67.5kHz dev. pilot 7.5kHz dev.
- Output level 0dB(1 μ V, 50 Ω /open terminal)

■ Arrangement of adjusting & test points

Cassette mechanism
(Surface)



Head section view



■ Information for using a car audio service jig

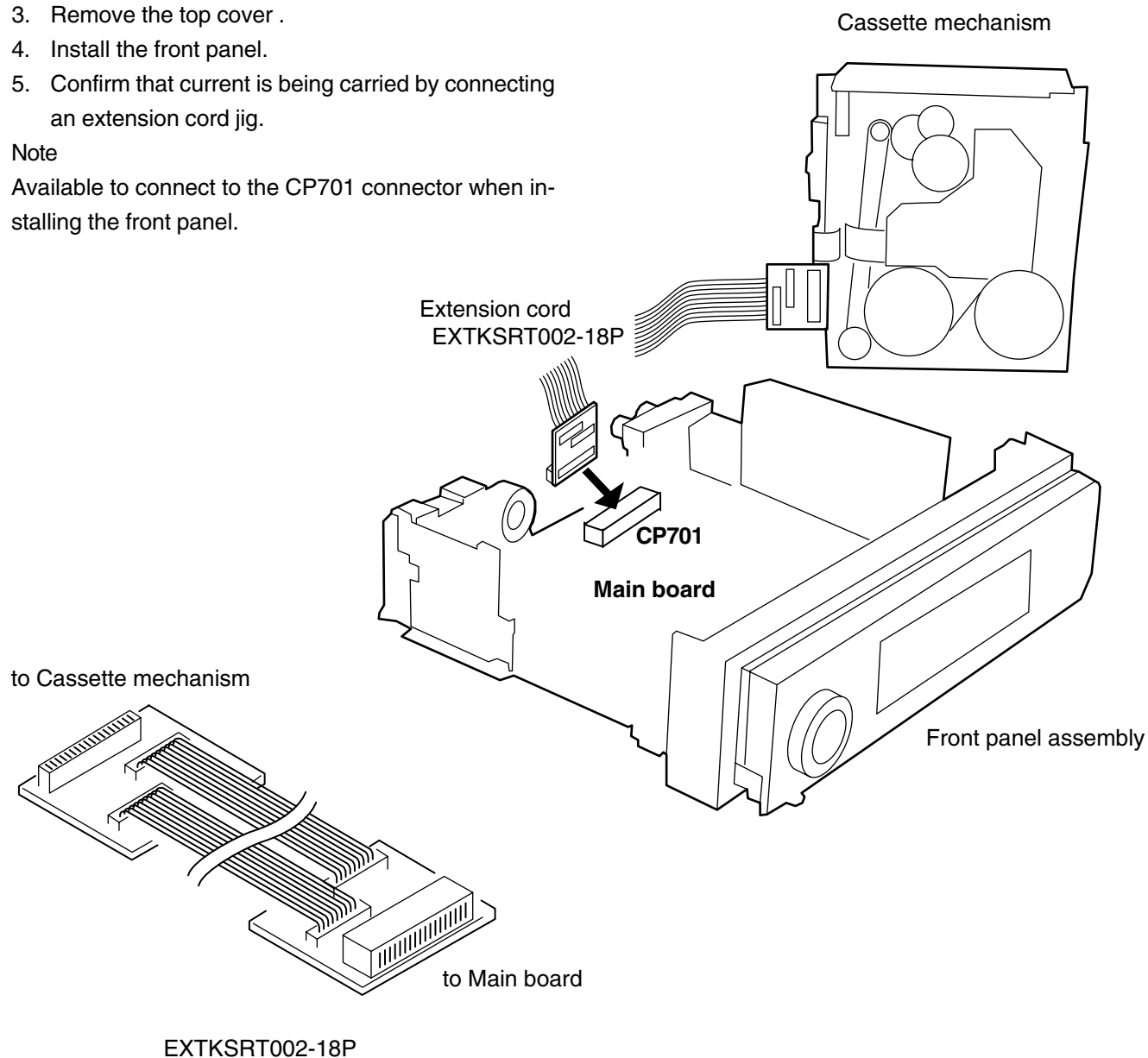
1. We're advancing efforts to make our extension cords common for all car audio products.
Please use this type of extension cord as follows.
2. As a U-shape type top cover is employed, this type of extension cord is needed to check operation of the mechanism assembly after disassembly.
3. Extension cord : EXTKSRT002-18P (18 pin extension cord) For connection between mechanism assembly and main board assembly.
Check for mechanism driving section such as motor ,etc..

■ Disassembly method

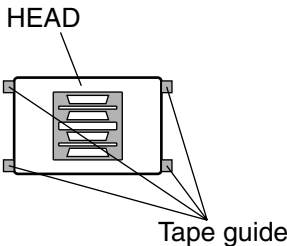
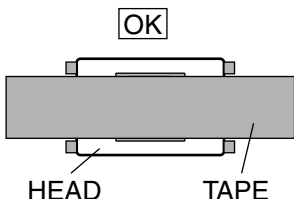
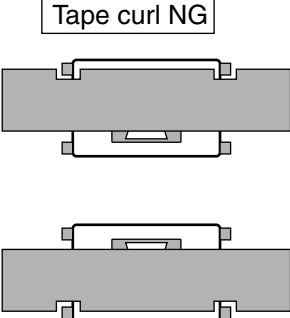
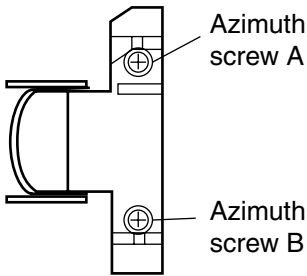
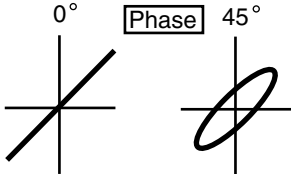
1. Remove the bottom cover.
2. Remove the front panel assembly.
3. Remove the top cover .
4. Install the front panel.
5. Confirm that current is being carried by connecting an extension cord jig.

Note

Available to connect to the CP701 connector when installing the front panel.



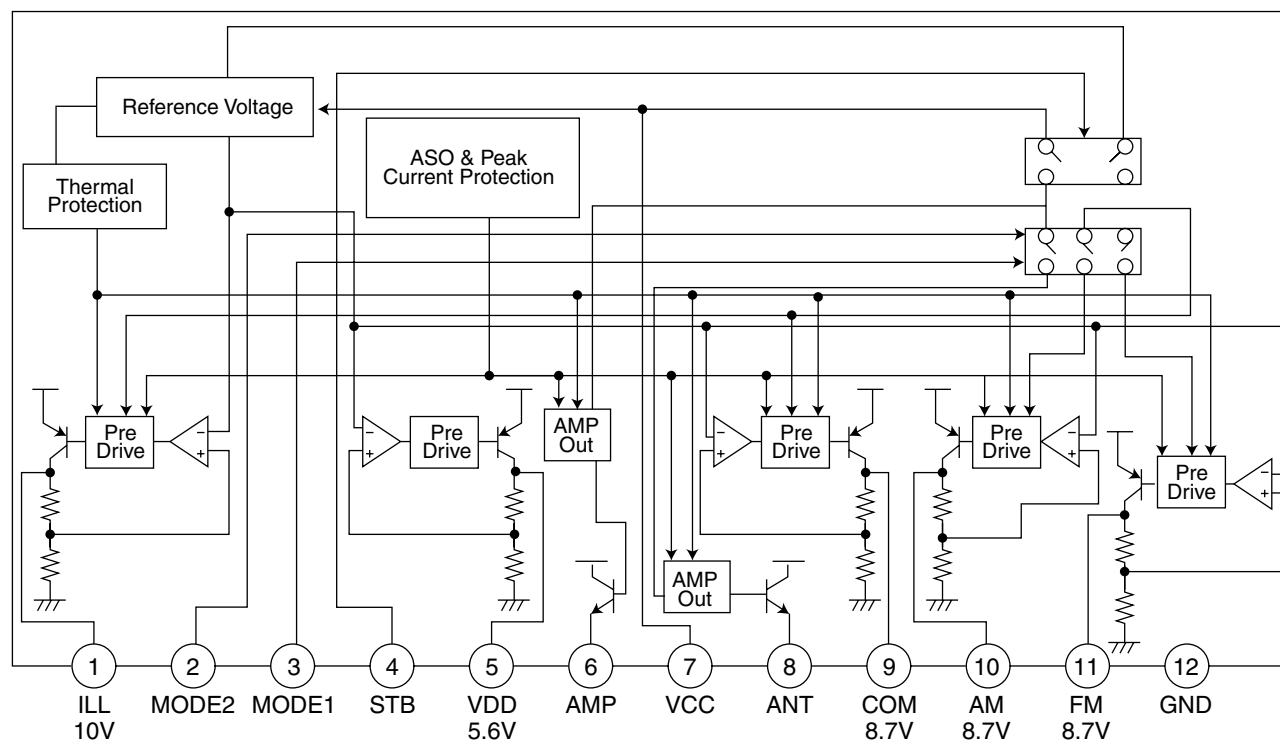
■ Mechanism adjustment section

Item	Adjusting & Confirmation Methods	Adjust	Std. Value
<p>1. Tape running adjustment</p> <p>2. Azimuth adjustment confirmation</p>	<p>a) At Forward playmode, using mirror tape, make adjustment with Azimuth screw A and Azimuth screw B, without curl of 4 parts of head tape guide.</p> <p>b) At Reverse play mode, using mirror tape, make adjustment with Azimuth screw A and Azimuth screw B, without tape curl of 4 parts of head tape guide.</p> <p>c) At Forward / Reverse play mode, make confirmation of no tape curl of 4 parts of head tape guide.</p> <p>a) At forward play mode, make adjustment of peak of Lch / Rch output with Azimuth screw A. * For Oscilloscope litharge corrugation, set 45° as standard.</p> <p>b) At Reverse play mode, make adjustment of peak of Lch / Rch output with Azimuth screw B. * For Oscilloscope litharge corrugation, set 45° as standard.</p> <p>c) With AC volt meter confirm the difference of output for 4ch between Lch / Rch at forward play mode and Lch / Rch Reverse play mode being within 3.0dB.</p> <p>d) After operation, make confirmation of Lch / Rch azimuth output being within 1.0dB from adjustment value.</p> <div style="text-align: center;">  <p>HEAD</p> <p>Tape guide</p> </div> <div style="display: flex; justify-content: space-around; margin-top: 20px;"> <div style="text-align: center;">  <p>OK</p> <p>HEAD TAPE</p> </div> <div style="text-align: center;">  <p>Tape curl NG</p> </div> </div>	<div style="text-align: right; margin-bottom: 20px;">  <p>Azimuth screw A</p> <p>Azimuth screw B</p> </div> <div style="text-align: center;">  <p>0° Phase 45°</p> </div>	
<p>2. Tape Speed and Wow & Flutter</p>	<p>1. Check to see if the reading of the frequency counter & Wow flutter meter is within 2940-3090 Hz(FWD/REV), and less than 0.35% (JIS RMS).</p> <p>2. In case of out of specification, adjust the motor with a built-in volume resistor .</p>	<p>Built-in volume resistor</p>	<p>Tape Speed 2940-3090Hz Wow&Flutter Less than 0.35% (JIS RMS)</p>
<p>3. Playback Frequency response</p>	<p>1. Play the test tape (VT724 : 1kHz) back and set the volume position at 2V .</p> <p>2. Play the test tape (VT739)back and confirm 0±3dB at 1kHz/10kHz and -4+2dB at 1kHz/63Hz.</p> <p>3. When 10kHz is out of specification, it will be necessary to read adjust the azimuth.</p>		<p>Speaker out 1kHz/10kHz : 0dB±3dB, 63Hz/1kHz : -4dB+2dB,</p>

Description of major ICs

■ AN80T05 (IC901) : Regulator

1. Terminal layout & Block diagram

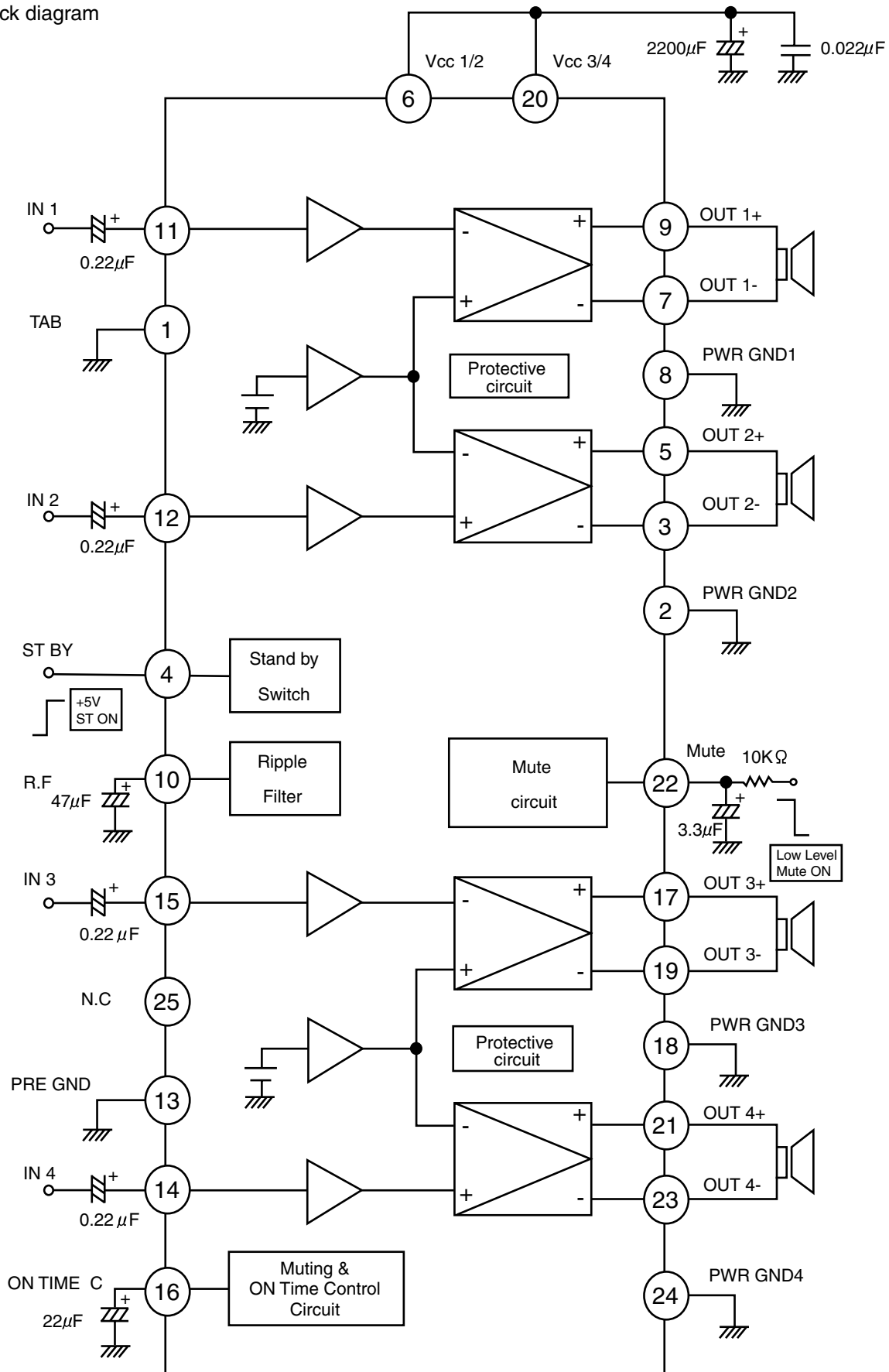


2. Pin function

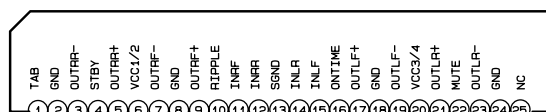
Pin No.	Symbol	Function
1	ILL	10V power supply for illumination.
2	MODE2	When 5V is input, becomes AM. and the antenna output is turned on.
3	MODE1	When 5V is input, becomes AM. and the output of FM is switched.
4	STB	When 5V is input, outputs to ILL, COM, and AMP. It is 0V usually.
5	VDD	5.6V power supply.
6	AMP	Power supply supply to remote amplifier
7	VCC	Back up. connects with ACC with it.
8	ANT	Power supply supply to auto antenna.
9	COM	8.7V power supply.
10	AM	The power supply of 8.7V to AM.
11	FM	The power supply of 8.7V to FM.
12	GND	Ground

■ LA4743K (IC301) : Power amp.

1. Block diagram



2. Terminal layout



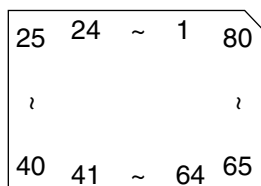
3. Pin function

LA4743B

Pin No.	Symbol	Function
1	TAB	Header of IC
2	GND	Power GND
3	OUTRR-	Output(-) for front Rch
4	STBY	Stand by input
5	OUTRR+	Output (+) for front Rch
6	VCC1/2	Power input
7	OUTRF-	Output (-) for rear Rch
8	GND	Power GND
9	OUTRF+	Output (+) for rear Rch
10	RIPPLE	Ripple filter
11	INRF	Rear Rch input
12	INRR	Front Rch input
13	SGND	Signal GND
14	INLR	Front Lch input
15	INLF	Rear Lch input
16	ONTIME	Power on time control
17	OUTLF+	Output (+) for rear Lch
18	GND	Power GND
19	OUTLF-	Output (-) for rear Lch
20	VCC3/4	Power input
21	OUTLR+	Output (+) for front
22	MUTE	Muting control input
23	OUTLR-	Output (-) for front
24	GND	Power GND
25	NC	Non connection

■ LC72366-9987 (IC701) : System CPU

1. Pin layout



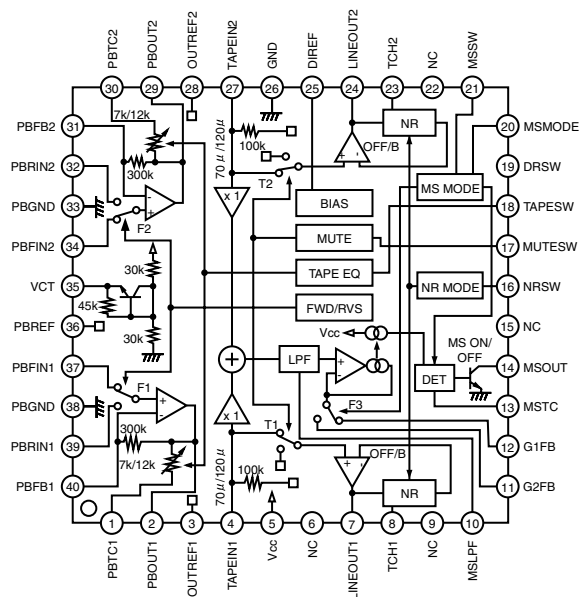
2. Pin function

Pin No.	Symbol	I/O	Function
1	XIN	I	Crystal oscillator input port
2	GND	-	Connect to GND
3	J-BUS SI	I	Data input for J-BUS information
4	J-BUS SO	O	Data output for J-BUS information
5	J-BUS SCK	O	Clock output for J-BUS information
6	J-BUS I/O	O	Switching signal output for J-BUS information I/O, H:Out L:In
7	TEL-MUTE	I	Telephone mute signal input
8	LCD SO	O	Data output for LCD driver
9	LCD SCK	O	Information clock output for LCD driver data
10	LCD CE	O	Chip enable output for LCD driver
11	DIMMER IN	-	None connection
12	EVOL SO	O	Data output for electrical volume
13	EVOL SCK	O	Clock output for electrical volume information
14	OPEN	I	Door open detect input
15	POWER ANT	I	Power ant ON signal input
16	MUTE	I	Mute signal input
17	CD ILLUM	-	Non connection
18	DEMEROUT	O	DIMMER control signal output
19	NC	-	Non connection
20	NC	-	Non connection
21	NC	-	Non connection
22	NC	-	Non connection
23	NC	-	Non connection
24	NC	-	Non connection
25	KS1	-	Non connection
26	KS0	O	Diode matrix output port for initial establishing
27	K3	I	Diode matrix output port for initial establishing
28	K2	I	Diode matrix output port for initial establishing
29	K1	-	Non connection
30	K0	I	Diode matrix output port for initial establishing
31	Vdd	-	5V power supply port (+B)
32	TEST	I	Turn on all light indicator of LCD, L: All light a LED indicator
33	FF/REW MODE	O	FF/REW mode select signal output
34	SEEK/STOP	O	H:Auto seek, L: Stop Use both as IF count REQ and Seek/Stop
35	MONO	O	Forced monaural output port, H:Turn on Forced monaural
36	RADIO/TAPE	-	Non connection
37	BEEP LEVEL	-	Non connection
38	PWR-CNT	O	"H" : Turn on power
39	ACC	-	Non connection
40	KICK	-	Non connection

Pin No.	Port Name	I/O	Function
41	MOTOR	O	Main motor output, H:Transport L: Stop
42	SUBMO+	O	Sub-motor output(+), Loading direction to transport output
43	SUBMO-	O	Sub-motor output(-), Eject direction to transport output
44	BEEP	-	Non connection
45	TAPE IN	I	Switch for detecting to input cassette, L: Cassette in
46	STANDBY	I	Switch for detecting standby position
47	REEL	I	Switch for detecting tape end position
48	MODE	I	Detecting mode position input
49	F/R	I	Switch for detecting forward/reverse , H:FWD L:REV
50	MS	I	MS input port,
51	SD/ST	I	Station detector, Stereo signal input, H:SD
52	DETACH	O	Front panel detect
53	ENC1	I	Connect to encoder 1
54	ENC2	I	Connect to encoder 2
55	J-BUS INT	I	Cut in signal detecting port from J-Bus information
56	REMOCON	-	Non connection
57	FM/AM	O	FM/AM mode switching signal port, H:FM L:AM
58	DOLBY	-	Non connection
59	NC	-	Non connection
60	NC	-	Non connection
61	MEM DET	I	Back-up power supply detecting port, H:input L:no input
62	LEVELMETER	I	Pressure voice level voltage input for level meter.
63	S.METER	I	S meter voltage input
64	KEY2	I	Key 2 input port
65	KEY1	I	Key 1 input port
66	KEY0	I	Key 0 input port
67	ACCDET	I	Hold port for Acc detecting, L: Hold mode
68	SENSE	I	Voltage sensor port
69	AM IF COUNT	-	Non connection
70	FM IF COUNT	I	FM frequency detecting
71	NC	-	Non connection
72	NC	-	Non connection
73	Vdd	-	5V power supply (+B)
74	AM OSC	I	Non connection
75	FM OSC	I	FM limited signal input
76	VSS	-	Ground port for power supply
77	NC	-	Non connection
78	E0	O	Error signal output port for PLL
79	TEST1	-	Test port for LSI, To connect ground
80	XOUT	O	4.5MHz crystal oscillator output

■ CXA2560Q (IC401) : Dolby B type noise reduction system with play back equalizer amp.

1. Pin layout & Block diagram



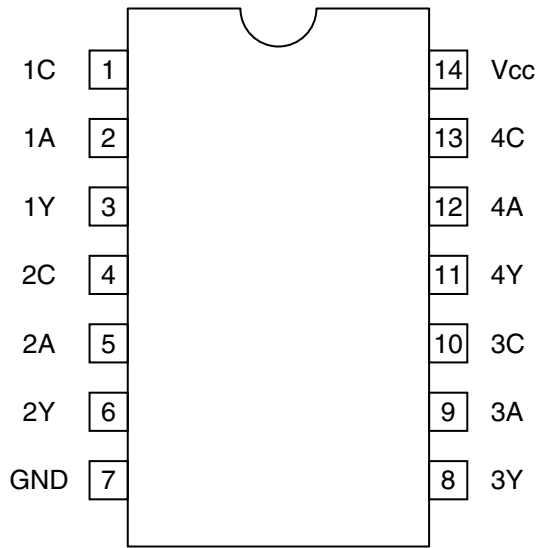
2. Pin function

Pin No.	Symbol	I/O	Function
1	PBTC1	-	Playback equalizer amplifier capacitance
2	PBOUT1	O	Playback equalizer amplifier output
3	OUTREF1	O	Output reference
4	TAPEIN1	I	TAPE input
5	Vcc	-	Power supply
6	NC	-	
7	LINEOUT1	O	Line output
8	TCH1	-	Time constant for the HLS
9	NC	-	
10	MSLPF	-	Cut-off frequency adjustment of the music sensor LPF
11	G2FB	-	Music signal interval detection
12	G1FB	-	Music signal interval detection
13	MSTC	-	Time constant for detecting music signal interval
14	MSOUT	O	Music sensor out
15	NC	-	No use
16	NRSW	I	Dolby NR control
17	MUTESW	I	Mute function control
18	TAPESW	I	Playback equalizer amplifier control
19	DRSW	I	Head select control
20	MSMODE	I	Music sensor mode control
21	MSSW	I	Music sensor control
22	NC	-	
23	TCH2	-	Time constant for the HLS
24	LINEOUT2	O	Line output

PinNo.	Symbol	I/O	Function
25	DIREF	-	Resistance for setting the reference
26	GND	-	Ground
27	TAPEIN2	I	TAPE input
28	OUTREF2	O	Output reference
29	PBOUT2	O	Playback equalizer amplifier output
30	PBTC2	-	Playback equalizer amplifier capacitance
31	PFBF2	I	Playback equalizer amplifier feedback
32	PBRIN2	I	Playback equalizer amplifier input
33	PBGND	-	Playback equalizer amplifier ground
34	PBFIN2	I	Playback equalizer amplifier input
35	VCT	O	Center
36	PBREF	O	Playback equalizer amplifier reference
37	PBFIN1	I	Playback equalizer amplifier input
38	PBGND	-	Playback equalizer amplifier ground
39	PBRIN1	I	Playback equalizer amplifier input
40	PBFB1	I	Playback equalizer amplifier feedback

■ HD74HC126P (IC801) : Changer control

1. Pin arrangement

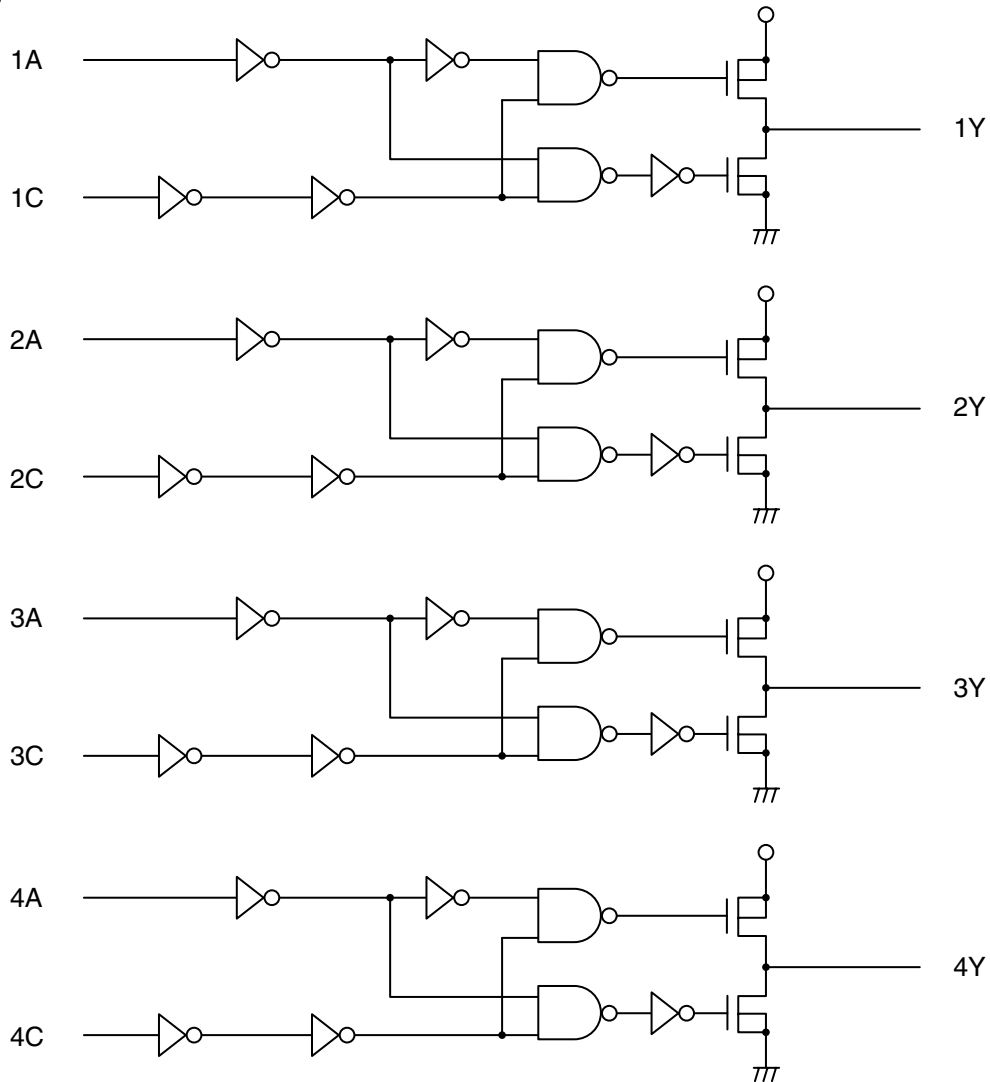


(TOP VIEW)

2. Pin function

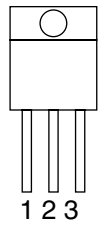
Input		Output
C	A	Y
L	X	Z
H	L	L
H	H	H

3. Block diagram

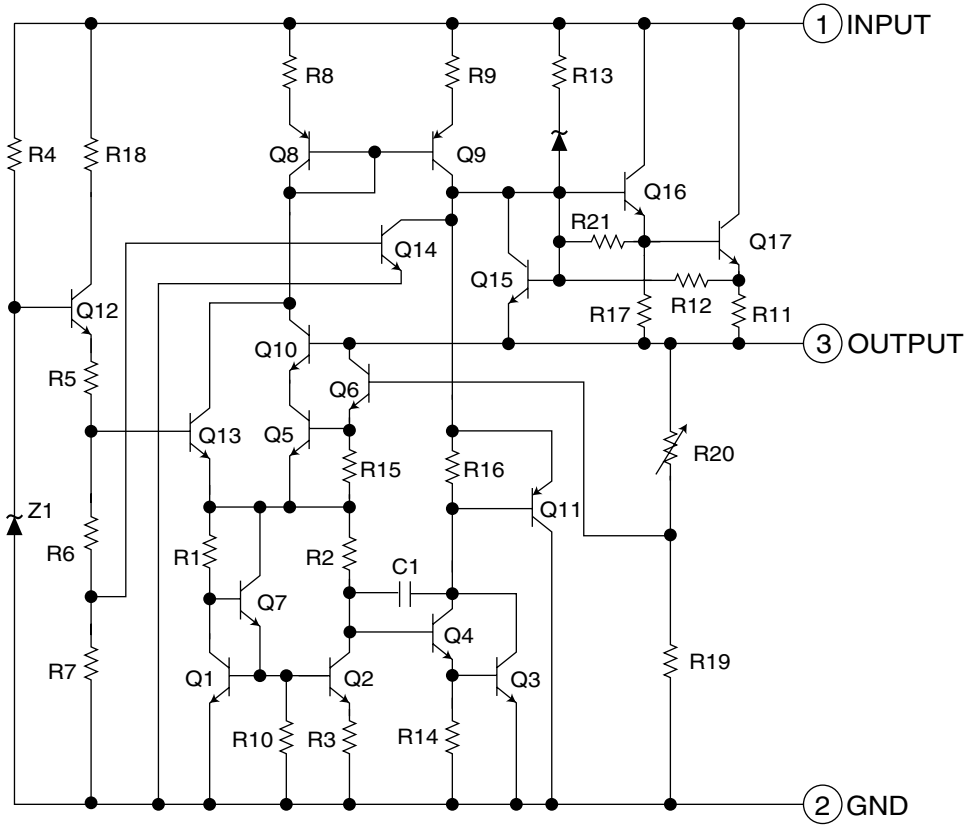


■ KIA7810PI (IC902) : Regulator

1.Pin layout

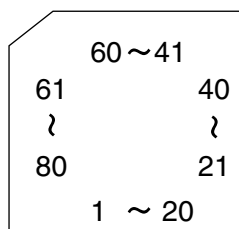


2.Block diagram

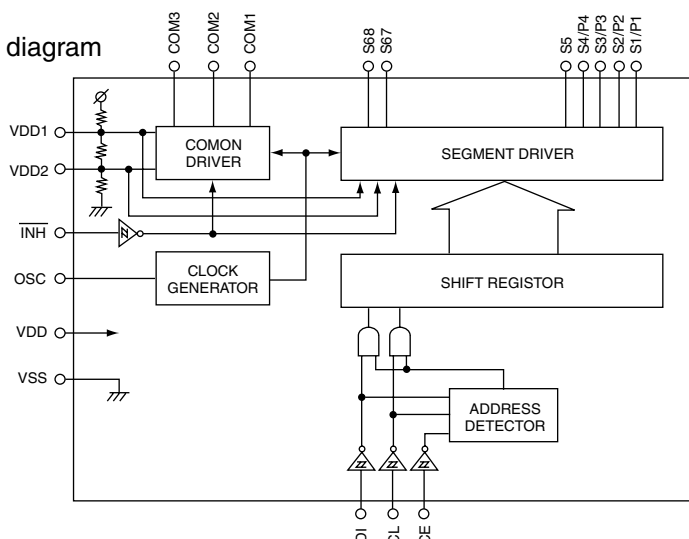


■ LC75873NW (IC601) : LCD driver

1.Pin layout



2.Block diagram

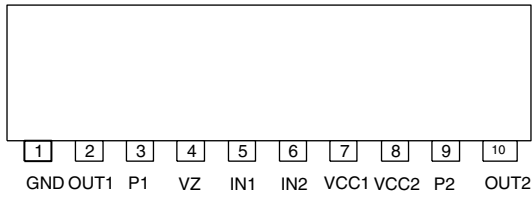


3.Pin function

Pin No.	Pin name	I/O	Description
79,80 1,2,3 to 66	S1/P1 TO S4/P4 S5 to S68	O	Segment outouts for displaying the display data transferred by serial data input. The S1/P1 to S4/P4 pins can be used as general-purpose output ports under serial data control.
67 78 69	COM1 COM2 COM3	O	Common driver outputs. The frame frequency f_0 is given by : $f_0 = (FOSC/384)Hz.$
74	OSC	I/O	Oscillator connection An oscillator circuit is formed by connecting an external resistor and capacitor to this pin.
76 77 78	CE CL DI	I	Serial data transfer inputs. Connected to the controller. CE:Chip enable CL:Synchronization clock DI:Transfer data
75	\overline{INH}	I	Display off control input <ul style="list-style-type: none"> • $\overline{INH} = "L"$(VSS) ... Display forced off S1/P1 to S4/P4 = "L" (These pins are forcibly set to the segment output port function and held at the low level.) S5 to S68 = "L" COM1 to COM3 "L" • $\overline{INH} = "H"$(HDD) ... Display on However, serial data transfer is possible when the display is forced off by this pin.
71	VDD1	I	Used for applying the LCD drive 2/3 bias voltage externally. Must be connected to VDD2 when a 1/2 bias drive scheme is used.
72	VDD2	I	Used for applying the LCD drive 1/3 bias voltage externally. Must be connected to VDD1 when a 1/2 bias drive scheme is used.
70	VDD	-	Power supply connection. Provide a voltage of between 3.0 and 6.0V.
73	VSS	-	Power supply connection. Connect to ground.

■ **LB1641 (IC402) : DC motor driver**

1. Pin layout

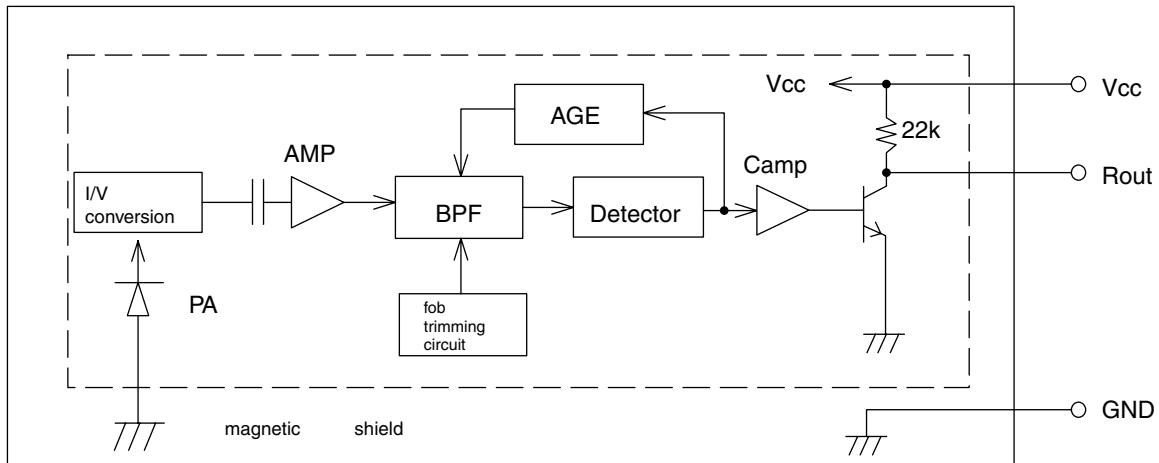


2. Pin function

Input		Output		Mode
IN1	IN2	OUT1	OUT2	
0	0	0	0	Brake
1	0	1	0	CLOCKWISE
0	1	0	1	COUNTER-CLOCKWISE
1	1	0	0	Brake

■ **RPM6938-SV4 (IC602) : Remote sensor**

1. Block diagram

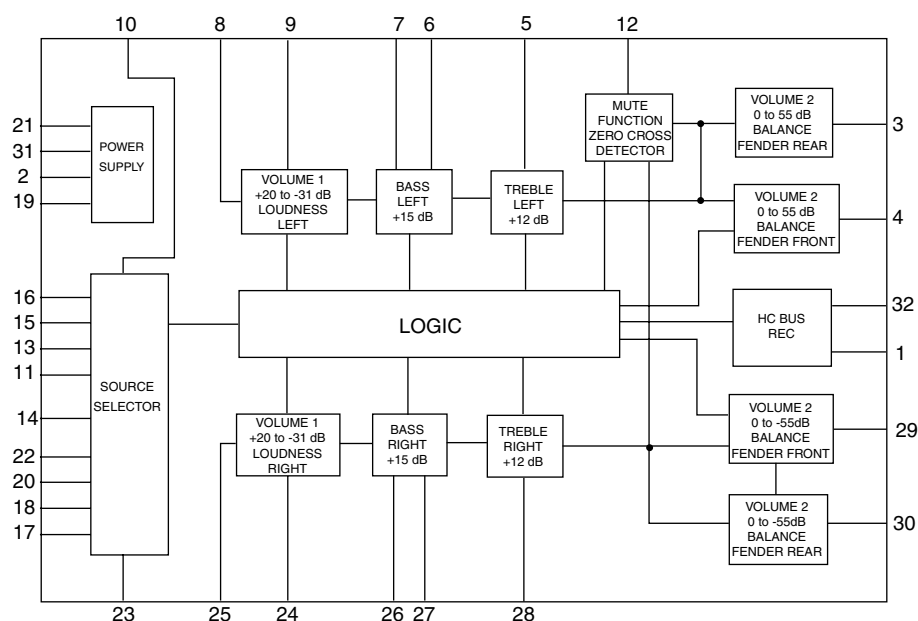


TEA6320T-X (IC161) : E.volume

1.Pin layout

SDA	1	32	SCL	
GND	2	31	VCC	
OUTLR	3	30	OUTRR	
OUTLF	4	29	OUTRF	
TL	5	28	TR	
B2L	6	27	B2R	
B1L	7	26	B1R	
IVL	8	25	IVR	
ILL	9	24	ILR	
QSL	10	23	QSR	
IDL	11	22	IDR	
MUTE	12	21	Vref	
ICL	13	CD-CH	20	ICR
IMD	14	19	CAP	
IBL	15	TAPE	18	IBR
IAL	16	TUNER	17	IAR

2.Block diagram



3.Pin functions

Pin No.	Symbol	I/O	Functions	Pin No.	Symbol	I/O	Functions
1	SDA	I/O	Serial data input/output.	17	IAR	I	Input A right source.
2	GND	-	Ground.	18	IBR	I	Input B right source.
3	OUTLR	O	output left rear.	19	CAP	-	Electronic filtering for supply.
4	OUTLF	O	output left front.	20	ICR	I	Input C right source.
5	TL	I	Treble control capacitor left channel or input from an external equalizer.	21	Vref	-	Reference voltage (0.5Vcc)
6	B2L	-	Bass control capacitor left channel or output to an external equalizer.	22	IDR	-	Not used
7	B1L	-	Bass control capacitor left channel.	23	QSR	O	Output source selector right channel.
8	IVL	I	Input volume 1. left control part.	24	ILR	I	Input loudness right channel.
9	ILL	I	Input loudness. left control part.	25	IVR	I	Input volume 1. right control part.
10	QSL	O	Output source selector. left channel.	26	B1R	-	Bass control capacitor right channel
11	IDL	-	Not used	27	B2R	O	Bass control capacitor right channel or output to an external equalizer.
12	MUTE	-	Not used	28	TR	I	Treble control capacitor right channel or input from an external equalizer.
13	ICL	I	Input C left source.	29	OUTRF	O	Output right front.
14	IMO	-	Not used	30	OUTRR	O	Output right rear.
15	IBL	I	Input B left source.	31	Vcc	-	Supply voltage.
16	IAL	I	Input A left source.	32	SCL	I	Serial clock input.



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